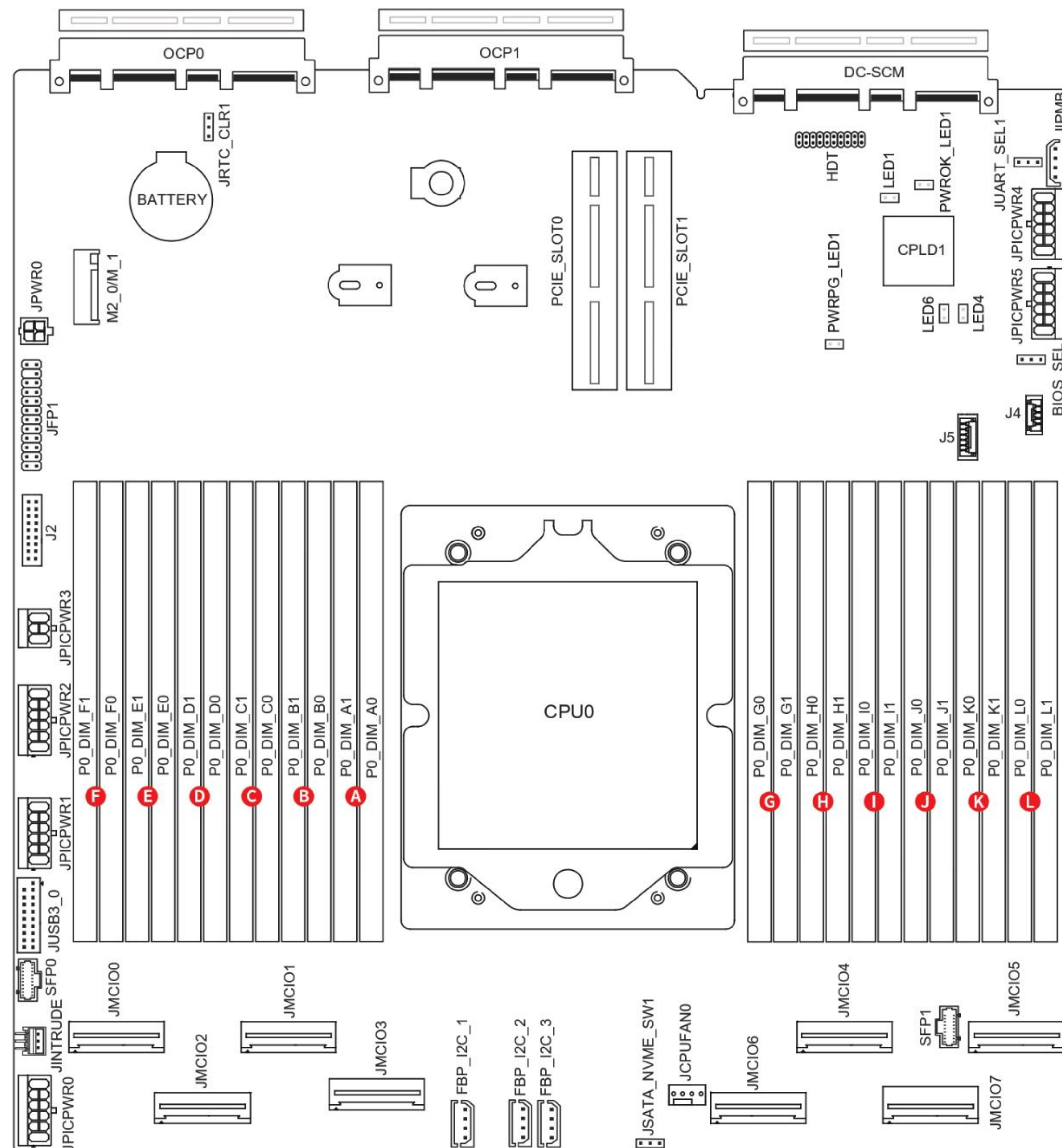


Vantageo 11T1

QUICK START GUIDE



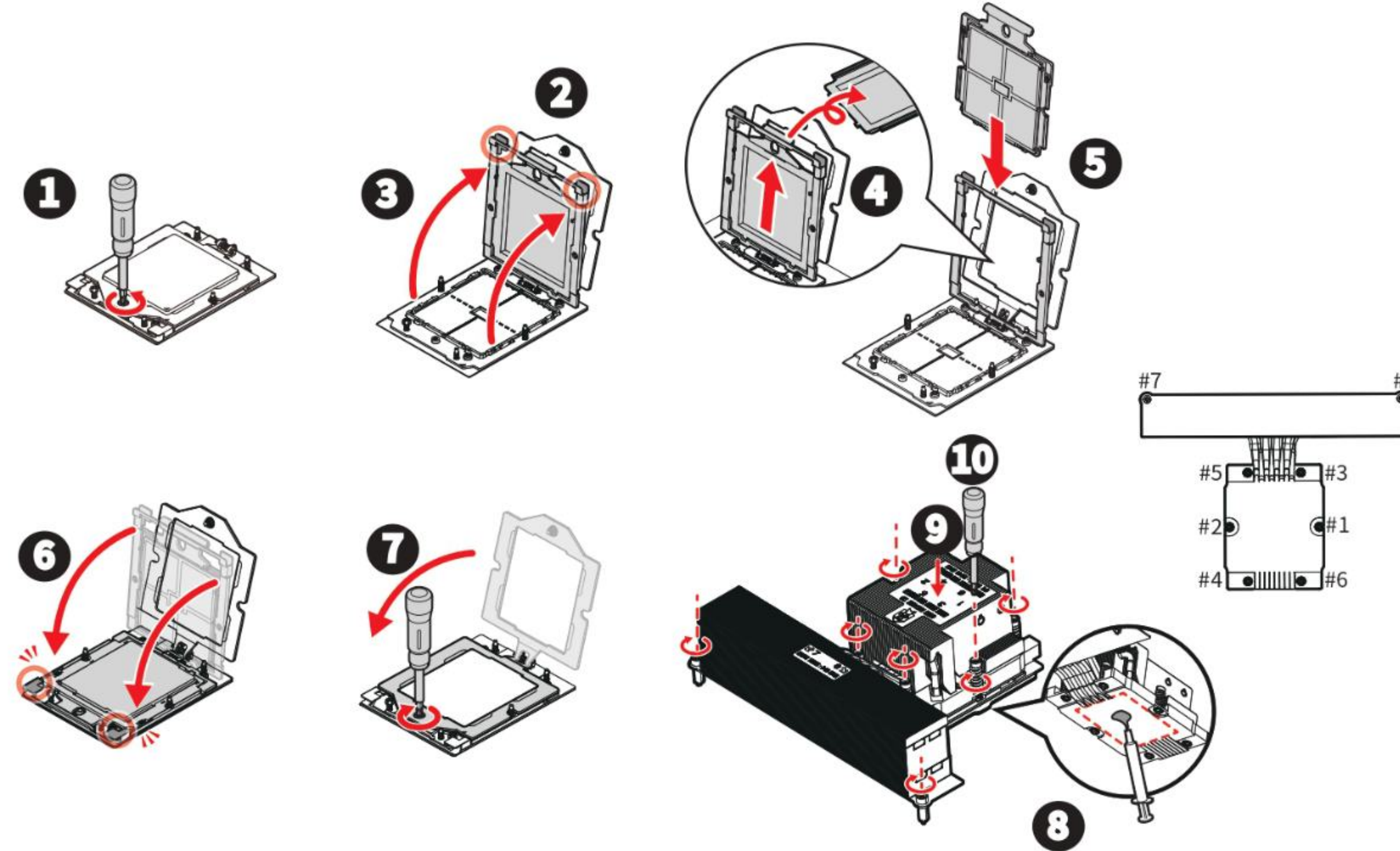
CPU

Single AMD EPYC™ 9004/9005 series processors(Socket SP5), max TDP 500W

DC-MHS Control Panel Header

 SFP1	1	P12V_AUX	2	GND
	3	P12V_AUX	4	BMC_SMB_LVC3_PESTI1_SDA
	5	NC	6	BMC_SMB_LVC3_PESTI1_SCL
	7	GND	8	GND
	9	USB2_BMC_PCP_ESD_D+	10	FM_FP_HPM_PCP_SB4
	11	USB2_BMC_PCP_ESD_D-	12	FM_FP_HPM_PCP_SB3
	13	GND	14	FM_FP_HPM_PCP_SB2
	15	SPI_BMC_FP_MISO_R	16	FM_FP_HPM_PCP_SB1
	17	SPI_BMC_FP_CS0_R	18	GND
	19	SPI_BMC_FP_MOSI_R	20	SPI_BMC_FP_CK_R

CPU and Heatsink Installation

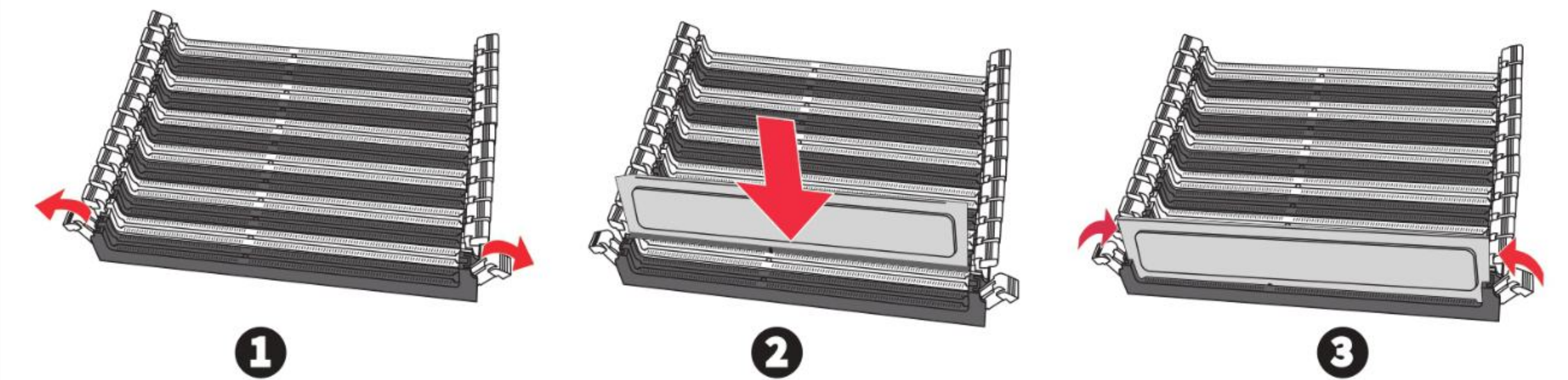


Connectors, Jumpers and LED Indicators

Name	Description
JPICPWR0	Power connector for fan board
JPICPWR4~5	Power connector for GPU
JPICPWR1~3	Power connector for HPM
JPWR0	Power connector for riser card
JMCIO0~7	MCIO 8i connectors
FBP_I2C_1~3	HDD BP SMBUS header
JCPUFAN0	CPU fan power connector
JUSB3_0	USB 3.0 internal connector
JIPMB	IPMB header
M2_0,M2_1	M.2 slots (M Key, PCIe 3.0 x2, 22110/ 2280)
JFP1	24-pin front panel connector
SFP0,SFP1	20-pin DC-MHS Control Panel header
PCIE_SLOT0~1	PCIe 5.0 x 16 slot (from CPU0)
OCP0~1	OCP 3.0 SFF (PCIe 5.0 x16 from CPU0)
DC-SCM	Port for DCSCM card
JINTRUDE	Chassis intrusion header
J2	PDB management connector
HDT	HDT connector
BIOS_SEL	System BIOS ROM and control by jumper
JUART_SEL_1	UART to CPU jumper
JSATA_NVME_SW1	JMCIO4/JMCIO5 setting jumper
CMOS_CLR	CMOS clear jumper
LED1	CPLD heart beat led
LED4	P3V3 power good led
LED6	P1V8 AUX power good Led
PWRPG_LED1	CPU power good Led
PWROK_LED1	CPU power OK Led

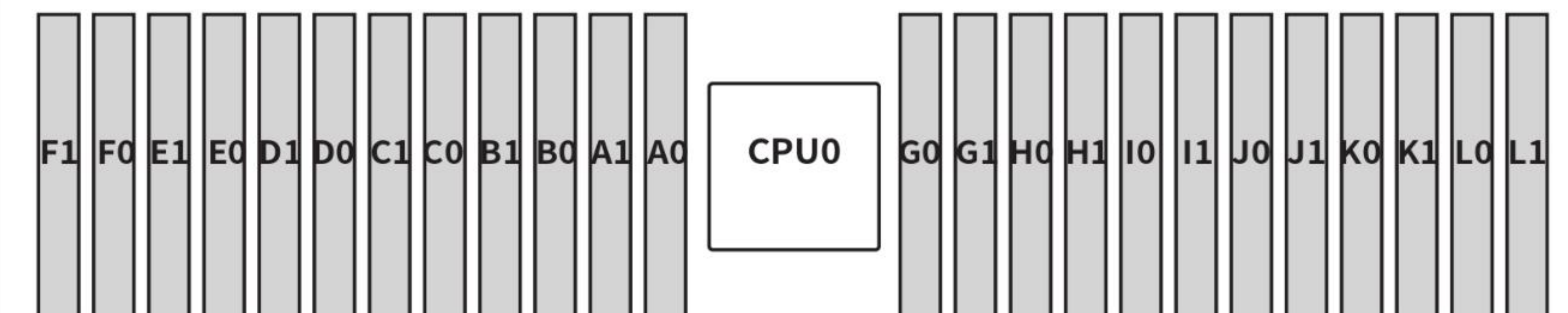
Memory Installation

1. Open the side clips to unlock the DIMM slot.
2. Insert the DIMM vertically into the slot, ensuring that the off-center notch at the bottom aligns with the slot.
3. Push the DIMM firmly into the slot until it clicks and the side clips automatically close.
4. Verify that the side clips have securely locked the DIMM in place.



DDR5 Only DIMM Configuration

The server board supports DDR5 RDIMMs and 3DS RDIMMs with memory speeds up to 5200 MT/s (2DPC).



1 CPU																										
Channel													C P U													
Qty. of DDR5	F1	F0	E1	E0	D1	D0	C1	C0	B1	B0	A1	A0		G0	G1	H0	H1	I0	I1	J0	J1	K0	K1	L0	L1	
24	V	V	V	V	V	V	V	V	V	V	V	V		V	V	V	V	V	V	V	V	V	V	V	V	V
12	V			V		V			V			V						V			V			V		V
10				V		V			V			V						V			V			V		
8				V					V			V						V						V		
6									V			V						V								
4									V											V						
2																				V						
1																										
"V" indicates DIMMs are populated with DDR5.																										



- There should be at least one DDR5 DIMM populated.
- Only support RDIMM,3DSRDIMM.
- JPICPWR4 and JPICPWR5 combined can support a maximum of 600W.
- DDR5 memory configurations requires **same DIMM types, ranks, speeds, and densities.**